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**FEB 08 2005****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Serial No.:</b>	10/708,039	<b>Conf. No.:</b>	2038
<b>Filing Date:</b>	02/04/2004	<b>Art Unit:</b>	2123
<b>Applicant:</b>	Maynard et al.	<b>Examiner:</b>	N/A
<b>Title:</b>	IC DESIGN MODELING ALLOWING DIMENSION-DEPENDENT RULE CHECKING	<b>Docket No.:</b>	BUR920030149US1 (IBMB-0034)

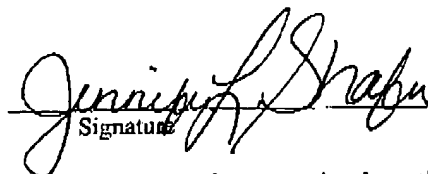
**COMMISSIONER FOR PATENTS****DESTINATION FACSIMILE NUMBER:** 703-872-9306

Transmitted herewith is: **Status Inquiry in 1 page**  
in the above identified application.

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I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date shown below.

Jennifer L. Shafer  
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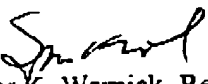
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## STATUS INQUIRY

Sir:

Applicant's representative respectfully requests the status of the above-referenced patent application.

Respectfully submitted,

  
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Date: February 8, 2005